



## 39. Course Specification of Analog Control Systems

<b>I. Course Identification and General Information:</b>						
1.	Course Title:	Analog Control Systems				
2.	Course Code & Number:	CCE332				
3.	Credit hours:	C.H				Total
		Th.	Tu.	Pr.	Tr.	
		2	2	2	-	4
4.	Study level/ semester at which this course is offered:	Fourth Year/ First Semester				
5.	Pre –requisite (if any):	Signal and <b>Systems</b> (CNE216)				
6.	Co –requisite (if any):	None.				
7.	Program (s) in which the course is offered:	Power Engineering and Electrical machines				
8.	Language of teaching the course:	English				
9.	Location of teaching the course:	Class				
10.	Prepared By:	Asst. Prof. Dr. Amin Abdelghani Mahyob				
11.	Date of Approval					

<b>II. Course Description:</b>	
<p>This introductory course, with lab, provides a theoretical and practical overview of classical analog control methods such as PID control and lag-lead control on automatic control. Students will learn how to apply the control theory to real engineering problems with Matlab and through laboratory experiments. Topics covered include characteristics, performance and design of feedback control systems in both time and frequency domains and examples of industrial</p>	

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III. Course Intended learning outcomes (CILOs) of the course		Referenced PILOs
a1	Identify the components of the control system, the effects produced by the different controllers and the suitable controller for a specified task	A1,A2,A3
a2	Determine the effect of a control action and its parameters on the stability, sensitivity, steady states and transient responses	A1
b1	Analyze the characteristics of dynamics systems and their performances.	B2,B3
b2	Assess system stability accuracy and sensitivity.	B1,B2,B3
c1	Design the different types of compensators and controllers based on the transient and steady state response criteria	C1,C2
c2	Apply experimentally and computer Aided Tools, (MATLAB, Simulink) in analyzing dynamics control systems and different controllers.	C3,C4
d1	Interact effectively with peers in the group	D1,D4
d2	Present project results to a technical audience and subdivide a project into relevant sub problems and solve it in a given time frame as a team.	D1,D3,D5

(A) Alignment Course Intended Learning Outcomes of Knowledge and Understanding to Teaching Strategies and Assessment Strategies:		
Course Intended Learning Outcomes	Teaching strategies	Assessment Strategies
a1. Identify the components of the control system, the effects produced by the different controllers and the suitable controller for a specified task	Lectures, Demonstrations, Software Simulation, Interactive class discussion.	Assignments, Oral Presentations, Quizzes, Tests, Written Exams.
a2. Determine the effect of a control action and its parameters on the stability,	Lectures, Demonstrations, Software Simulation, Interactive class discussion.	Assignments, Oral Presentations,

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sensitivity, steady states and transient responses		Quizzes, Tests, Written Exams.
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**(B) Alignment Course Intended Learning Outcomes of Intellectual Skills to Teaching Strategies and Assessment Strategies:**

Course Intended Learning Outcomes	Teaching strategies	Assessment Strategies
<b>b1.</b> Analyze the characteristics of dynamics systems and their performances.	Lectures, Demonstrations, Software Simulation, Interactive class discussion.	Assignments, Oral Presentations, Quizzes, Tests, Written Exams.
<b>b2.</b> Assess system stability accuracy and sensitivity.	Lectures, Demonstrations, Software Simulation, Interactive class discussion.	Assignments, Oral Presentations, Quizzes, Tests, Written Exams.

**(C) Alignment Course Intended Learning Outcomes of Professional and Practical Skills to Teaching Strategies and Assessment Strategies:**

Course Intended Learning Outcomes	Teaching strategies	Assessment Strategies
<b>c1.</b> Design the different types of compensators and controllers based on the transient and steady state response criteria	Lectures, Demonstrations, Software Simulation, Interactive class discussion.	Assignments, Oral Presentations, Quizzes, Tests, Written Exams.
<b>c2.</b> Apply experimentally and computer Aided Tools, (MATLAB, Simulink) in analyzing dynamics control systems and different controllers.	Lectures, Demonstrations, Software Simulation, Interactive class discussion.	Assignments, Oral Presentations, Quizzes, Tests, Written Exams.

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<b>(D) Alignment Course Intended Learning Outcomes of Transferable Skills to Teaching Strategies and Assessment Strategies:</b>		
Course Intended Learning Outcomes	Teaching strategies	Assessment Strategies
<b>d1.</b> Interact effectively with peers in the group	Demonstrations, Software Simulation Interactive class discussion.	Assignments, Oral Presentations
<b>d2.</b> Present project results to a technical audience (Communication) and subdivide a project into relevant sub problems and solve it in a given time frame as a team. (Teamwork).	Software Simulation, Interactive class discussion.	Assignments, Oral Presentations.

<b>IV. Course Content:</b>					
<b>A – Theoretical Aspect:</b>					
Order	Units/Topics List	Learning Outcomes	Sub Topics List	Number of Weeks	Contact hours
1.	Introduction to Control Systems	a1, a2, b1, b2,	<ul style="list-style-type: none"> <li>▪ Examples of Control Systems</li> <li>▪ Regulator control system and position (servomechanism) control system</li> <li>▪ SISO, MIMO systems</li> <li>▪ Closed-Loop Control Versus Open-Loop Control</li> <li>▪ Design of Control Systems</li> </ul>	1	2

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2.	Performance (response) of Control Systems	a1, a2, b1, b2	<ul style="list-style-type: none"> <li>▪ Test Input Signals</li> <li>▪ Performance of First-Order Systems</li> <li>▪ Performance of Second-Order Systems</li> <li>▪ Effect of feedback on the performance of the system</li> <li>▪ Control system specifications</li> <li>▪ Effects of adding a new Pole or Zero on the System Response</li> <li>▪ The s-Plane Root Location and the Transient Response</li> <li>▪ The response of high order systems</li> <li>▪ The Simplification of Linear Systems</li> <li>▪ Performance Indices</li> </ul>	2	4
3.	Characteristics of Feedback Control Systems	b1,b2,	<ul style="list-style-type: none"> <li>▪ Configurations of feedback control systems</li> <li>▪ Effects of controllers on the control system</li> <li>▪ Stability analysis of control system:</li> <li>▪ Relation between pole locations and stability</li> <li>▪ Routh-Hurwitz criteria for stability</li> <li>▪ Sensitivity of Control Systems to Parameter Variations</li> </ul>	2	4

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			<ul style="list-style-type: none"> <li>▪ Rejection of disturbance</li> <li>▪ Control of the Transient Response</li> <li>▪ Steady-State Error</li> </ul>		
4.	The Root Locus Analysis	a1, a2, b1, b2, d2	<ul style="list-style-type: none"> <li>▪ The Root Locus Concept (angle and magnitude criteria) and Procedure</li> <li>▪ Stability and the root locus</li> <li>▪ Transient response and root locus</li> <li>▪ Negative Gain Root Locus</li> <li>▪ Implementation of controllers using operational amplifier</li> </ul>	1	2
5.	Frequency response analysis	a2, b1, b2, d1	<ul style="list-style-type: none"> <li>▪ Frequency Response Plots:(Bod diagrams, Polar plot, Log Magnitude versus Phase plot)</li> <li>▪ Frequency Response Measurements</li> <li>▪ Stability in the Frequency Domain (absolute stability "Nyquist criterion", Relative stability)</li> <li>▪ Performance Specifications in the Frequency Domain</li> <li>▪ Closed loop frequency response</li> <li>▪ Relations between performance Specifications</li> </ul>	3	6

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			in the time and Frequency Domains		
6.	Feedback Control Design	b1,b2,c1,c2	<ul style="list-style-type: none"> <li>▪ Approaches to System Design</li> <li>▪ Design of: Phase lead, phase lag, lag-lead and PID Controllers using the Root Locus</li> <li>▪ Design of: Phase lead, phase lag, lag-lead and PID Controllers using the Frequency Response</li> <li>▪ System Design Using Integration Networks</li> <li>▪ Systems with a Prefilter</li> <li>▪ Design for Deadbeat Response</li> </ul>	3	6
7.	Modern Control Design (State variables design)	All CILO's	<ul style="list-style-type: none"> <li>▪ Pole placement design</li> <li>▪ Ackermann's formula</li> <li>▪ State Estimation (observer)</li> <li>▪ Closed loop characteristic equation</li> <li>▪ Reduced order estimator</li> <li>▪ Controllability and Observability</li> <li>▪ Systems with inputs</li> </ul>	2	4
<b>Number of Weeks /and Units Per Semester</b>				<b>14</b>	<b>28</b>

**B – Tutorial Aspect:**

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Order	Tasks/ Experiments	Number of Weeks	Contact hours	Learning Outcomes
1.	<ul style="list-style-type: none"> <li>Linear and nonlinear systems</li> <li>Introduction to Control Systems</li> <li>SISO, MIMO systems</li> <li>Open-Loop and Closed-Loop</li> <li>Simple examples of Control Systems.</li> </ul>	a1, a2, b1, b2,	1	2
2.	<u>Performance (response) of Control Systems:</u> <ul style="list-style-type: none"> <li>Test Input Signals</li> <li>Performance of First-Order Systems</li> <li>Performance of Second-Order Systems</li> <li>Effect of feedback signal on the performance of the system response</li> <li>Control system specifications and pole locations)</li> <li>Effects of a Third Pole and a Zero on the Second-Order System Response</li> <li>The response of high order systems</li> <li>Reduced model of Linear Systems</li> </ul>	a1, a2, b1, b2, d1, d2	2	4
3.	<u>Characteristics of Feedback Control Systems:</u> <ul style="list-style-type: none"> <li>Effects of controllers on transient and steady responses)</li> <li>Stability analysis of control system:(, Routh-Hurwitz criterion for stability)</li> <li>Sensitivity of Control Systems</li> <li>Disturbance rejection</li> <li>Steady-State Error</li> </ul>	a1, a2, b1, b2, d1, d2	2	4

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4.	<u>The Root Locus Method:</u> <ul style="list-style-type: none"> <li>• Root locus plot</li> <li>• Stability and the root locus</li> <li>• Transient response and root locus</li> <li>• Negative Gain Root Locus</li> <li>• Implementation of controllers using operational amplifier</li> </ul>	a1, a2, b1, b2, d1, d2	1	2
5.	<u>Frequency Response Methods:</u> <ul style="list-style-type: none"> <li>• Frequency Response Plots:(Bod diagrams, Polar plot, Log Magnitude versus Phase plot)</li> <li>• Minimum phase and non-minimum phase systems</li> <li>• Frequency Response Measurements</li> <li>• Stability in the Frequency Domain (Nyquist criterion, Relative stability margins)</li> </ul>	a1, a2, b1, b2, d1, d2	2	4
6.	<u>Feedback Control Design</u> <ul style="list-style-type: none"> <li>• Approaches to System Design</li> <li>• Design of: Phase lead, phase lag, lag-lead and PID Controllers using the Root Locus</li> <li>• Design of: Phase lead, phase lag, lag-lead and PID Controllers using the Frequency Response</li> <li>• System Design Using Integration Networks</li> <li>• Systems with a Prefilter</li> <li>• Design for Deadbeat Response</li> </ul>	a1, a2, b1, b2, c1, c2, d1, d2	2.5	5
7.	<u>Modern Control Design (State variables design)</u> <u>Modern)</u> <ul style="list-style-type: none"> <li>• Pole placement design</li> <li>• Ackermann's formula</li> <li>• State Estimation (observer)</li> <li>• Closed loop characteristic equation</li> </ul>	a1, a2, b1, b2, c1, c2, d1, d2	1.5	3

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	<ul style="list-style-type: none"> <li>• Reduced order estimator</li> <li>• Controllability and Observability</li> <li>• Systems with inputs</li> </ul>			
8.	<u>MatLab – Control Toolbox:</u> <ul style="list-style-type: none"> <li>• Computer Usage MATLAB software with Control Toolbox</li> </ul>	a1, a2, b1, b2, c1, c2, d1, d2	2	4
<b>Number of Weeks /and Units Per Semester</b>			<b>14</b>	<b>28</b>

<b>C - Practical Aspect:</b>				
<b>Order</b>	<b>Tasks/ Experiments</b>	<b>Number of Weeks</b>	<b>Contact hours</b>	<b>Learning Outcomes</b>
1.	<ul style="list-style-type: none"> <li>• Introduction to Control System Lab</li> <li>• Lab safety rules and regulations</li> </ul> <u><b>Controlled systems</b></u> <ul style="list-style-type: none"> <li>• The controlled system, definition of items</li> <li>• Types of controlled systems, characteristics, examples</li> </ul>	1	2	a1,b1
2.	<u><b>Controllers</b></u> <ul style="list-style-type: none"> <li>• Definitions, characteristics</li> </ul> <u><b>P Controller</b></u> <ul style="list-style-type: none"> <li>• Characteristic of P controller</li> <li>• The proportional (P) controller response for various controlled system transfer coefficient</li> <li>• Relation between reference variable and proportionality coefficient Kp</li> </ul>	1	2	b1,b2,c1,c2
3.	<u><b>Delayed closed loop control system of first order and second order</b></u> Definition of elements, Producing delay elements	1	2	b1,b2,c1,c2

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	Determination of controlled system TF coefficients Controllability of the system			
4.	<b><u>PI controller</u></b> <ul style="list-style-type: none"> <li>characteristic I element, Response of I controller to positive and negative signals</li> <li>Characteristics of PI controller, Step response of PI controller, Parallel and Series configuration</li> </ul>	1	2	b1,b2,c1,c2
5.	<b><u>PD controller</u></b> <ul style="list-style-type: none"> <li>Characteristics of PD controller,</li> <li>Explanation of the derivative action,</li> <li>Step response, of PI controller, Parallel and Series configuration</li> </ul>	1	2	a1,b2,c1,c2,d1
6.	<b><u>PID controller</u></b> <ul style="list-style-type: none"> <li>Characteristics of PID controller</li> <li>step response</li> <li>ramp response</li> <li>parallel series configurations</li> </ul>	1	2	a2,b2,c1,c2,d2
7.	<b><u>P,PI,PD,PID controllers (stability, optimization)</u></b> <ul style="list-style-type: none"> <li>Stability and stability margins</li> <li>Maximum overshoot</li> <li>Settling time</li> <li>Optimization</li> </ul>		2	b1,b2,c1,c2
8.	<b><u>P,PI,PD,PID controllers (Ziegler- Nichols recommendations)</u></b> <ul style="list-style-type: none"> <li>The critical proportionality coefficient <math>K_{pcri}</math></li> <li>The critical oscillation period <math>T_{cri}</math></li> <li>Setting recommendations according to <u>Ziegler and Nichols</u></li> </ul>	1	2	b1,b2,c1,c2

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9.	<u>P,PI,PD,PID controllers (parallel and series configurations)</u> <ul style="list-style-type: none"> <li>Advanced calculations for parallel configuration (mathematical controller)</li> <li>Advanced calculations for series configuration (technical controller)</li> </ul>	1	2	b1,b2,c1,c2
10.	<u>P,PI,PD,PID controllers (closed loop control of fixed value, sequence and program)</u> <ul style="list-style-type: none"> <li>Fixed value control, Sequence control, Time Program control</li> <li>PID controller in a PT-4 controlled system</li> <li>PI controller in a PT-4 controlled system</li> <li>PD controller in a PT-4 controlled system</li> <li>P controller in a PT-4 controlled system</li> </ul>	1	2	b1,b2,c1,c2
11.	<u>Frequency response measurements:</u> <ul style="list-style-type: none"> <li>Measure and plot the frequency response of a RLC circuit,</li> <li>determine the transfer function of the system</li> <li>determine the parameters of the circuits</li> </ul>	1	2	a2,b2,c1,c2
12.	<u>Using MATLAB, Control Toolbox, and Simulink:</u> <ul style="list-style-type: none"> <li>Study the time and frequency response of dynamic system</li> <li>Determine the speed of response and accuracy</li> </ul> Root locus and Bode Plots	1	2	b1,b2,c1,c2,d2

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	<ul style="list-style-type: none"> <li>Parameter Identification, P, PI, PD/PID Control, Position Control, regulator control</li> <li>Steady State Error and Stability Analysis</li> </ul>			
13.	<p><b>Projects:</b>                  Student make groups of 3-4 students each group is required to choose a lab project (build, simulate, make a proto type) of a control system. The flowing are some examples of industrial control systems</p> <ul style="list-style-type: none"> <li>- Temperature Control with a two-Position Controller</li> <li>- Temperature Control with a PID Controller</li> <li>- Liquid Level Control with a two-Position Controller</li> <li>- Liquid Level Control with a PID Controller</li> <li>- DC. Motor Speed Control</li> <li>- Voltage Regulation of a dc generator</li> <li>- Position Control System</li> <li>- Speed Control System</li> </ul>	2	4	b1,b2,c1,c2,d1
<b>Number of Weeks /and Units Per Semester</b>		<b>14</b>	<b>28</b>	
<b>V. Teaching strategies of the course:</b>				
<ul style="list-style-type: none"> <li>Lectures</li> <li>Interactive class discussion</li> <li>Demonstration</li> <li>Software Simulation</li> </ul>				

<b>VI. Assignments:</b>				
Order	Assignments	Aligned CILOs (symbols)	Week Due	Mark

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1.	Open-Loop and closed-Loop	a1, c1,d2	2	2.5
2.	Effects of controllers on transient and steady responses)	a2, c2,d2	4	2.5
3.	Design of: Phase lead, phase lag, lag-lead and PID Controllers using the Frequency Response	c1, d1, d2	8	2.5
4.	Pole placement design and Ackermann's formula	b1, c2, d2	12	2.5
5.	MATLAB Applications	b2, c2, d1, d2	14	10
<b>Total</b>				<b>20</b>

<b>VII. Schedule of Assessment Tasks for Students During the Semester:</b>					
No.	Assessment Method	Week Due	Mark	Proportion of Final Assessment	Aligned Course Learning Outcomes
1	Quizzes	4 <sup>th</sup> , 7 <sup>th</sup> , 10 <sup>th</sup> , and 13 <sup>th</sup>	20	10%	a1,a2,b1,b2
2	Assignments	Weekly	20	10%	a1,a2,b1,b2,d2
3	Mid-Term exam	7 <sup>th</sup>	30	15%	a1,a2,b1,b2
4	Practical exam	15 <sup>th</sup>	30	15%	b1,b2,c1,c2
5	Final Exam theory	16 <sup>th</sup>	100	50%	a1,a2,b1,b2
<b>Total</b>			<b>200</b>	<b>100%</b>	

<b>VIII. Learning Resources:</b>	
<i>Written in the following order: ( Author - Year of publication – Title – Edition – Place of publication – Publisher).</i>	
<b>1- Required Textbook(s) ( maximum two ).</b>	
<ol style="list-style-type: none"> <li>1. Charles L. Philips and Royce Harbor " Feedback control systems", fourth edition, Prentice Hall.</li> <li>2. Richard C. Dorf and Robert H. Bishop, Modern Control Systems (12th Edition), Prentice Hall.</li> </ol>	

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2- Essential References.	
	<ol style="list-style-type: none"> <li>1. K. Ogata, 1997, Modern Control Engineering, 3rd Edition, Prentice Hall.-</li> <li>2. Norman S. Nise, 2011, Control Systems Engineering (Sixth edition).</li> <li>3. Supplemental materials - Schaum's outline of theory and problems of feedback and control systems, by McGraw Hill Publisher.</li> </ol>
3- Electronic Materials and Web Sites etc.	
	<ol style="list-style-type: none"> <li>1. Modelica Association (2000). ModelicaTM - A Unified Object-Oriented Language for Physical Systems Modeling. Tutorial Version 1.4 (ModelicaTutorial14.pdf). Available from: <a href="https://modelica.org/documents/">https://modelica.org/documents/</a></li> <li>2. MapleSim Video Tutorial: Modelica Video lectures available form: <a href="http://www.youtube.com/watch?v=reehU1dzeDc">http://www.youtube.com/watch?v=reehU1dzeDc</a>.</li> <li>3. Simulink-Matlab tutorial for beginners Video lectures available form:</li> <li>4. <a href="http://www.youtube.com/results?search_query=simulink+tutorial+for+beginners&amp;oq=simulink&amp;gs_l=youtube.1.9.0i10.337429.342148.0.351270.8.8.0.0.0.0.738.2481.3j3-2j2j0j1.8.0...0.0...1ac.1.11.youtube.iIK7kMX6hfo-">http://www.youtube.com/results?search_query=simulink+tutorial+for+beginners&amp;oq=simulink&amp;gs_l=youtube.1.9.0i10.337429.342148.0.351270.8.8.0.0.0.0.738.2481.3j3-2j2j0j1.8.0...0.0...1ac.1.11.youtube.iIK7kMX6hfo-</a></li> </ol>

IX. Course Policies:	
1.	<p><b>Class Attendance:</b></p> <p>-A student should attend not less than 75 % of total hours of the subject; otherwise he will not be able to take the exam and will be considered as exam failure. If the student is absent due to illness, he/she should bring <b>an approved</b> statement from university Clinic</p>
2.	<p><b>Tardy:</b></p> <p>- For late in attending the class, the student will be initially notified. If he repeated lateness in attending class he will be considered as absent.</p>
3.	<p><b>Exam Attendance/Punctuality:</b></p> <p>- A student should attend the exam on time. He is Permitted to attend an exam half one hour from exam beginning, after that he/she will not be permitted to take the exam and he/she will be considered as absent in exam.</p>
4.	<p><b>Assignments &amp; Projects:</b></p>

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	- The assignment is given to the students after each chapter; the student has to submit all the assignments for checking on time.
5.	<b>Cheating:</b> - For cheating in exam, a student will be considered as <b>failure</b> . In case the cheating is repeated three times during his/her study the student will be disengaged from the Faculty.
6.	<b>Plagiarism:</b> Plagiarism is the attending of a student the exam of a course instead of another student. If the examination committee <b>proved</b> a plagiarism of a student, he will be disengaged from the Faculty. The final disengagement of the student from the Faculty should be confirmed from the Student Council Affair of the university.
7.	<b>Other policies:</b> - Mobile phones are not allowed to use during a class lecture. It must be closed, otherwise the student will be asked to leave the lecture room - Mobile phones are not allowed in class during the examination. Lecture notes and assignments my given directly to students using soft or hard copy

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	<b><u>Deputy Rector for Academic Affairs Asst. Prof. Dr. Ibrahim AlMutaa</u></b> <b><u>Assoc. Prof. Dr. Ahmed Mujahed</u></b> <b><u>Asst. Prof. Dr. Munasar Alsubri</u></b>

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## 39. Template for Course Plan of Analog Control Systems

Information about Faculty Member Responsible for the Course:							
Name of Faculty Member	Dr. Amin Abdelghani H. Mahyob	Office Hours					
Location & Telephone No.	770249615	SAT	SUN	MON	TUE	WED	THU
E-mail	amin.mahyob@gmail.com						

II. Course Identification and General Information:						
1.	Course Title:	Analog Control Systems				
2.	Course Code & Number:	CCE332				
3.	Credit hours:	C.H				Total
		Th.	Tu.	Pr	Tr.	
		2	2	2	-	4
4.	Study level/ semester at which this course is offered:	Fourth year/First Semester				
5.	Pre –requisite (if any):	Signal and systems (CNE216)				
6.	Co –requisite (if any):	None.				
7.	Program (s) in which the course is offered:	Electrical power and machines				
8.	Language of teaching the course:	English				
9.	System of Study:	Regular				
10.	Mode of delivery:	Semesters.				
11.	Location of teaching the course:	Class				

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### III. Course Description:

This introductory course, with lab, provides a theoretical and practical overview of classical analog control methods such as PID control and lag-lead control on automatic control. Students will learn how to apply the control theory to real engineering problems with Matlab and through laboratory experiments. Topics covered include characteristics, performance and design of feedback control systems in both time and frequency domains and examples of industrial.

### IV. Intended learning outcomes (ILOs) of the course:

- Brief summary of the knowledge or skill the course is intended to develop:
  1. Identify the components of the control system, the effects produced by the different controllers and the suitable controller for a specified task
  2. Determine the effect of a control action and its parameters on the stability, sensitivity, steady states and transient responses
  3. Analyze the characteristics of dynamics systems and their performances.
  4. Assess system stability accuracy and sensitivity.
  5. Design the different types of compensators and controllers based on the transient and steady state response criteria
  6. Apply experimentally and computer Aided Tools, (MATLAB, Simulink) in analyzing dynamics control systems and different controllers.
  7. Interact effectively with peers in the group
  8. Present project results to a technical audience and subdivide a project into relevant sub problems and solve it in a given time frame as a team.

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<b>V. Course Content:</b>				
<b>A – Theoretical Aspect:</b>				
<b>Order</b>	<b>Units/Topics List</b>	<b>Sub Topics List</b>	<b>Number of Weeks</b>	<b>Contact hours</b>
1.	Introduction to Control Systems	<ul style="list-style-type: none"> <li>▪ Examples of Control Systems</li> <li>▪ Regulator control system and position (servomechanism) control system</li> <li>▪ SISO, MIMO systems</li> <li>▪ Closed-Loop Control Versus Open-Loop Control</li> <li>▪ Design of Control Systems</li> </ul>	1 <sup>st</sup>	2
2.	Performance (response) of Control Systems	<ul style="list-style-type: none"> <li>▪ Test Input Signals</li> <li>▪ Performance of First-Order Systems</li> <li>▪ Performance of Second-Order Systems</li> <li>▪ Effect of feedback on the performance of the system</li> <li>▪ Control system specifications</li> <li>▪ Effects of adding a new Pole or Zero on the System Response</li> <li>▪ The s-Plane Root Location and the Transient Response</li> <li>▪ The response of high order systems</li> <li>▪ The Simplification of Linear Systems</li> <li>▪ Performance Indices</li> </ul>	2 <sup>nd</sup> ,3 <sup>rd</sup>	4
3.	Characteristics of Feedback Control Systems	<ul style="list-style-type: none"> <li>▪ Configurations of feedback control systems</li> <li>▪ Effects of controllers on the control system</li> <li>▪ Stability analysis of control system:</li> </ul>	4 <sup>th</sup> , 5 <sup>th</sup>	4

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		<ul style="list-style-type: none"> <li>▪ Relation between pole locations and stability</li> <li>▪ Routh-Hurwitz criteria for stability</li> <li>▪ Sensitivity of Control Systems to Parameter Variations</li> <li>▪ Rejection of disturbance</li> <li>▪ Control of the Transient Response</li> <li>▪ Steady-State Error</li> </ul>		
4.	The Root Locus Analysis	<ul style="list-style-type: none"> <li>▪ The Root Locus Concept (angle and magnitude criteria) and Procedure</li> <li>▪ Stability and the root locus</li> <li>▪ Transient response and root locus</li> <li>▪ Negative Gain Root Locus</li> <li>▪ Implementation of controllers using operational amplifier</li> </ul>	6 <sup>th</sup>	2
5.	Midterm exam	<ul style="list-style-type: none"> <li>▪</li> </ul>	7 <sup>th</sup>	2
6.	Frequency response analysis	<ul style="list-style-type: none"> <li>▪ Frequency Response Plots:(Bod diagrams, Polar plot, Log Magnitude versus Phase plot)</li> <li>▪ Frequency Response Measurements</li> <li>▪ Stability in the Frequency Domain (absolute stability "Nyquist criterion", Relative stability)</li> <li>▪ Performance Specifications in the Frequency Domain</li> <li>▪ Closed loop frequency response</li> <li>▪ Relations between performance Specifications in the time and Frequency Domains</li> </ul>	8 <sup>th</sup> , 9 <sup>th</sup> , 10 <sup>th</sup>	6
7.	Feedback Control Design	<ul style="list-style-type: none"> <li>▪ Approaches to System Design</li> </ul>	11 <sup>th</sup> , 12 <sup>th</sup> , 13 <sup>th</sup>	6

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		<ul style="list-style-type: none"> <li>▪ Design of: Phase lead, phase lag, lag-lead and PID Controllers using the Root Locus</li> <li>▪ Design of: Phase lead, phase lag, lag-lead and PID Controllers using the Frequency Response</li> <li>▪ System Design Using Integration Networks</li> <li>▪ Systems with a Prefilter</li> <li>▪ Design for Deadbeat Response</li> </ul>		
8.	Modern Control Design (State variables design)	<ul style="list-style-type: none"> <li>▪ Pole placement design</li> <li>▪ Ackermann's formula</li> <li>▪ State Estimation (observer)</li> <li>▪ Closed loop characteristic equation</li> <li>▪ Reduced order estimator</li> <li>▪ Controllability and Observability</li> <li>▪ Systems with inputs</li> </ul>	14 <sup>th</sup> ,15 <sup>th</sup>	4
9.	Final exam.		16 <sup>th</sup>	2
<b>Number of Weeks /and Units Per Semester</b>			<b>16</b>	<b>32</b>

<b>B – Tutorial Aspect:</b>			
<b>Order</b>	<b>Tasks/ Experiments</b>	<b>Contact hours</b>	<b>Learning Outcomes</b>
1.	<ul style="list-style-type: none"> <li>• Linear and nonlinear systems</li> <li>• Introduction to Control Systems</li> <li>• SISO, MIMO systems</li> <li>• Open-Loop and Closed-Loop</li> <li>• Simple examples of Control Systems.</li> </ul>	1 <sup>st</sup>	2
2.	<u>Performance (response) of Control Systems:</u> <ul style="list-style-type: none"> <li>• Test Input Signals</li> </ul>	2 <sup>nd</sup> ,3 <sup>rd</sup>	4

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	<ul style="list-style-type: none"> <li>• Performance of First-Order Systems</li> <li>• Performance of Second-Order Systems</li> <li>• Effect of feedback signal on the performance of the system response</li> <li>• Control system specifications and pole locations)</li> <li>• Effects of a Third Pole and a Zero on the Second-Order System Response</li> <li>• The response of high order systems</li> <li>• Reduced model of Linear Systems</li> </ul>		
3.	<u>Characteristics of Feedback Control Systems:</u> <ul style="list-style-type: none"> <li>• Effects of controllers on transient and steady responses)</li> <li>• Stability analysis of control system:(, Routh-Hurwitz criterion for stability)</li> <li>• Sensitivity of Control Systems</li> <li>• Disturbance rejection</li> <li>• Steady-State Error</li> </ul>	4 <sup>th</sup> ,5 <sup>th</sup>	4
4.	<u>The Root Locus Method:</u> <ul style="list-style-type: none"> <li>• Root locus plot</li> <li>• Stability and the root locus</li> <li>• Transient response and root locus</li> <li>• Negative Gain Root Locus</li> <li>• Implementation of controllers using operational amplifier</li> </ul>	6 <sup>th</sup>	2
5.	<u>Frequency Response Methods:</u> <ul style="list-style-type: none"> <li>• Frequency Response Plots:(Bod diagrams, Polar plot, Log Magnitude versus Phase plot)</li> <li>• Minimum phase and non-minimum phase systems</li> <li>• Frequency Response Measurements</li> <li>• Stability in the Frequency Domain (Nyquist criterion, Relative stability margins)</li> </ul>	7 <sup>th</sup> ,8 <sup>th</sup>	4

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<b>6.</b>	<u>Feedback Control Design</u> <ul style="list-style-type: none"> <li>• Approaches to System Design</li> <li>• Design of: Phase lead, phase lag, lag-lead and PID Controllers using the Root Locus</li> <li>• Design of: Phase lead, phase lag, lag-lead and PID Controllers using the Frequency Response</li> <li>• System Design Using Integration Networks</li> <li>• Systems with a Prefilter</li> <li>• Design for Deadbeat Response</li> </ul>	9 <sup>th</sup> , 10 <sup>th</sup> 11 <sup>th</sup>	5
<b>7.</b>	<u>Modern Control Design (State variables design) Modern</u> <ul style="list-style-type: none"> <li>• Pole placement design</li> <li>• Ackermann's formula</li> <li>• State Estimation (observer)</li> <li>• Closed loop characteristic equation</li> <li>• Reduced order estimator</li> <li>• Controllability and Observability</li> <li>• Systems with inputs</li> </ul>	11 <sup>th</sup> ,12 <sup>th</sup>	3
<b>8.</b>	<u>MatLab – Control Toolbox:</u> <ul style="list-style-type: none"> <li>• Computer Usage MATLAB software with Control Toolbox</li> </ul>	13 <sup>th</sup> ,14 <sup>th</sup>	4
<b>Number of Weeks /and Units Per Semester</b>		<b>14</b>	<b>28</b>

<b>C - Practical Aspect: (if any)</b>				
Order	Tasks/ Experiments	Number of Weeks	Contact hours	Learning Outcomes
<b>1.</b>	<ul style="list-style-type: none"> <li>• Introduction to Control System Lab</li> <li>• Lab safety rules and regulations</li> <li>• <u>Controlled systems</u></li> <li>• The controlled system, definition of items</li> </ul>	1 <sup>st</sup>	2	a1,b1

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	<ul style="list-style-type: none"> <li>Types of controlled systems, characteristics, examples</li> </ul>			
2.	<p><b><u>Controllers</u></b></p> <ul style="list-style-type: none"> <li>Definitions, characteristics</li> </ul> <p><b><u>P Controller</u></b></p> <ul style="list-style-type: none"> <li>Characteristic of P controller</li> <li>The proportional (P) controller response for various controlled system transfer coefficient</li> <li>Relation between reference variable and proportionality coefficient <math>K_p</math></li> </ul>	2 <sup>nd</sup>	2	b1,b2,c1,c2
3.	<p><b><u>Delayed closed loop control system of first order and second order</u></b></p> <p>Definition of elements, Producing delay elements</p> <p>Determination of controlled system TF coefficients</p> <p>Controllability of the system</p>	3 <sup>rd</sup>	2	b1,b2,c1,c2
4.	<p><b><u>PI controller</u></b></p> <ul style="list-style-type: none"> <li>characteristic I element, Response of I controller to positive and negative signals</li> <li>Characteristics of PI controller, Step response of PI controller, Parallel and Series configuration</li> </ul>	4 <sup>th</sup>	2	b1,b2,c1,c2
5.	<p><b><u>PD controller</u></b></p> <ul style="list-style-type: none"> <li>Characteristics of PD controller,</li> <li>Explanation of the derivative action,</li> <li>Step response, of PI controller, Parallel and Series configuration</li> </ul>	5 <sup>th</sup>	2	a1,b2,c1,c2,d1
6.	<p><b><u>PID controller</u></b></p> <ul style="list-style-type: none"> <li>Characteristics of PID controller</li> <li>step response</li> <li><b>ramp response</b></li> </ul>	6 <sup>th</sup>	2	a2,b2,c1,c2,d2

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	<ul style="list-style-type: none"> <li>parallel series configurations</li> </ul>			
7.	<u>P,PI,PD,PID controllers (stability, optimization)</u> <ul style="list-style-type: none"> <li>Stability and stability margins</li> <li>Maximum overshoot</li> <li>Settling time</li> <li>Optimization</li> </ul>	7 <sup>th</sup>	2	b1,b2,c1,c2
8.	<u>P,PI,PD,PID controllers (Ziegler- Nichols recommendations)</u> <ul style="list-style-type: none"> <li>The critical proportionality coefficient <math>K_{pcri}</math></li> <li>The critical oscillation period <math>T_{cri}</math></li> <li>Setting recommendations according to <b>Ziegler and Nichols</b></li> </ul>	8 <sup>th</sup>	2	b1,b2,c1,c2
9.	<u>P,PI,PD,PID controllers (parallel and series configurations)</u> <ul style="list-style-type: none"> <li>Advanced calculations for parallel configuration (mathematical controller)</li> <li>Advanced calculations for series configuration (technical controller)</li> </ul>	9 <sup>th</sup>	2	b1,b2,c1,c2
10.	<u>P,PI,PD,PID controllers (closed loop control of fixed value, sequence and program)</u> <ul style="list-style-type: none"> <li>Fixed value control, Sequence control, Time Program control</li> <li>PID controller in a PT-4 controlled system</li> <li>PI controller in a PT-4 controlled system</li> <li>PD controller in a PT-4 controlled system</li> <li>P controller in a PT-4 controlled system</li> </ul>	10 <sup>th</sup>	2	b1,b2,c1,c2

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11.	<p><b><u>Frequency response measurements:</u></b></p> <ul style="list-style-type: none"> <li>• Measure and plot the frequency response of a RLC circuit,</li> <li>• determine the transfer function of the system</li> <li>• determine the parameters of the circuits</li> </ul>	11 <sup>th</sup>	2	a2,b2,c1,c2
12.	<p><b><u>Using MATLAB, Control Toolbox, and Simulink:</u></b></p> <ul style="list-style-type: none"> <li>• Study the time and frequency response of dynamic system</li> <li>• Determine the speed of response and accuracy Root locus and Bode Plots</li> <li>• Parameter Identification, P, PI, PD/PID Control, Position Control, regulator control</li> <li>• Steady State Error and Stability Analysis</li> </ul>	12 <sup>th</sup>	2	b1,b2,c1,c2,d2
13.	<p><b><u>Projects:</u></b>                  Student make groups of 3-4 students each group is required to choose a lab project (build, simulate, make a proto type) of a control system. The flowing are some examples of industrial control systems</p> <ul style="list-style-type: none"> <li>- Temperature Control with a two-Position Controller</li> <li>- Temperature Control with a PID Controller</li> <li>- Liquid Level Control with a two-Position Controller</li> <li>- Liquid Level Control with a PID Controller</li> <li>- DC. Motor Speed Control</li> <li>- Voltage Regulation of a dc generator</li> <li>- Position Control System</li> <li>- Speed Control System</li> </ul>	13 <sup>th</sup> ,14 <sup>th</sup>	4	b1,b2,c1,c2,d1

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<b>Number of Weeks /and Units Per Semester</b>	<b>14</b>	<b>28</b>	
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<b>VI. Teaching strategies of the course:</b>	
<ul style="list-style-type: none"> <li>• Lectures</li> <li>• Interactive class discussion</li> <li>• Demonstration</li> <li>• Software Simulation</li> </ul>	

<b>VII. Assignments:</b>				
Order	Assignments	Aligned CILOs (symbols)	Week Due	Mark
1.	Open-Loop and closed-Loop	a1, c1,d2	2	2.5
2.	Effects of controllers on transient and steady responses)	a2, c2,d2	4	2.5
3.	Design of: Phase lead, phase lag, lag-lead and PID Controllers using the Frequency Response	c1, d1, d2	8	2.5
4.	Pole placement design and Ackermann's formula	b1, c2, d2	12	2.5
5.	MATLAB Applications	b2, c2, d1, d2	14	10
<b>Total</b>				<b>20</b>

<b>VIII. Schedule of Assessment Tasks for Students During the Semester:</b>				
No.	Assessment Method	Week Due	Mark	Proportion of Final Assessment
1.	Quizzes	4 <sup>th</sup> ,7 <sup>th</sup> ,10 <sup>th</sup> , and 13 <sup>th</sup>	20	10%
2.	Assignments	Weekly	20	10%
3.	Mid-Term exam	7 <sup>th</sup>	30	15%

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4.	Practical exam	15 <sup>th</sup>	30	15%
5.	Final Exam theory	16 <sup>th</sup>	100	50%
	<b>Total</b>		<b>200</b>	<b>100%</b>

## IX. Learning Resources:

*Written in the following order: ( Author - Year of publication – Title – Edition – Place of publication – Publisher).*

### 1- Required Textbook(s) ( maximum two ).

1. Charles L. Philips and Royce Harbor " Feedback control systems", fourth edition, Prentice Hall.
2. Richard C. Dorf and Robert H. Bishop, Modern Control Systems (12th Edition), Prentice Hall.-

### 2- Essential References.

1. K. Ogata, 1997, Modern Control Engineering, 3rd Edition, Prentice Hall.-
2. Norman S. Nise, 2011, Control Systems Engineering (Sixth edition).
3. Supplemental materials - Schaum's outline of theory and problems of feedback and control systems, by McGraw Hill Publisher.

### 3- Electronic Materials and Web Sites etc.

1. Modelica Association (2000). Modelica™ - A Unified Object-Oriented Language for Physical Systems Modeling. Tutorial Version 1.4 (ModelicaTutorial14.pdf). Available from: <https://modelica.org/documents/>
2. MapleSim Video Tutorial: Modelica Video lectures available form: <http://www.youtube.com/watch?v=reehU1dzeDc>.
3. Simulink-Matlab tutorial for beginners Video lectures available form:
4. [http://www.youtube.com/results?search\\_query=simulink+tutorial+for+beginners&eq=simulink&gs\\_l=youtube.1.9.0110.337429.342148.0.351270.8.8.0.0.0.0.738.2481.3j3-2j2j0j1.8.0...0.0...1ac.1.11.youtube.iIK7kMX6hfo-](http://www.youtube.com/results?search_query=simulink+tutorial+for+beginners&eq=simulink&gs_l=youtube.1.9.0110.337429.342148.0.351270.8.8.0.0.0.0.738.2481.3j3-2j2j0j1.8.0...0.0...1ac.1.11.youtube.iIK7kMX6hfo-)

## X. Course Policies:

- |    |                          |
|----|--------------------------|
| 1. | <b>Class Attendance:</b> |
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	-A student should attend not less than 75 % of total hours of the subject; otherwise he will not be able to take the exam and will be considered as exam failure. If the student is absent due to illness, he/she should bring <b>an approved</b> statement from university Clinic
2.	<b>Tardy:</b> - For late in attending the class, the student will be initially notified. If he repeated lateness in attending class he will be considered as absent.
3.	<b>Exam Attendance/Punctuality:</b> - A student should attend the exam on time. He is Permitted to attend an exam half one hour from exam beginning, after that he/she will not be permitted to take the exam and he/she will be considered as absent in exam.
4.	<b>Assignments &amp; Projects:</b> - The assignment is given to the students after each chapter; the student has to submit all the assignments for checking on time.
5.	<b>Cheating:</b> - For cheating in exam, a student will be considered as fail. In case the cheating is repeated three times during his/her study the student will be disengaged from the Faculty.
6.	<b>Plagiarism:</b> Plagiarism is the attending of a student the exam of a course instead of another student. If the examination committee <b>proved</b> a plagiarism of a student, he will be disengaged from the Faculty. The final disengagement of the student from the Faculty should be confirmed from the Student Council Affair of the university.
7.	<b>Other policies:</b> - Mobile phones are not allowed to use during a class lecture. It must be closed, otherwise the student will be asked to leave the lecture room - Mobile phones are not allowed in class during the examination. Lecture notes and assignments my given directly to students using soft or hard copy

## 40. Course Specification of Computer Architecture and Organization

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I. Course Identification and General Information						
1.	Course Title:	Computer Architecture and Organization				
2.	Course Code & Number:	CCE315				
3.	Credit hours:	C.H			TOTAL	
		Th.	Tu.	Pr.		Tr.
		2	2	-	-	3
4.	Study level/ semester at which this course is offered:	Fourth Year/ First Semester				
5.	Pre –requisite (if any):	Microprocessors & Assembly Language (CCE214)				
6.	Co –requisite (if any):	None.				
7.	Program (s) in which the course is offered:	B.Sc. of Computer and Control Engineering				
8.	Language of teaching the course:	Arabic & English				
9.	Location of teaching the course:	Class Room (Faculty of Engineering)				
10.	Prepared By:	Prof. Abdul Raqib Abdo Asaad				
11.	Date of Approval					

II. Course Description
<p>This course covers computer architecture, organization, performance, computer components, computer memory system, I/O modules, instruction sets, processor structure and function, control unit operation, and parallel organization.</p> <p>This course depends on lectures and tutorials parts. In addition, it depends on Microprocessor and Assembly Language, and Programming Language (II) as prerequisites.</p>

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<b>III. Course Aims</b>	
<b>This course aims to:</b>	
<ol style="list-style-type: none"> <li>1. Understand the difference between computer organization and computer architecture.</li> <li>2. Understand the concepts of computer performance and how to evaluate the performance of a digital computer.</li> <li>3. Gain knowledge about computer memory system and I/O subsystems.</li> <li>4. Understand the structure and function of computer processor.</li> <li>5. Have knowledge of parallel organization and parallel processing.</li> </ol>	

<b>IV. Course Intended learning outcomes (CILOs) of the course</b>		<b>Referenced PILOs</b>
<b>a1</b>	Classify and list the different types of computer organizations and architectures.	A1 (E)
<b>a2</b>	Recognize the efficiency design of a digital computer and its units.	A2 (E)
<b>b1</b>	Compose the main units of a digital computer.	B1 (E)
<b>b2</b>	Evaluate the different solutions to enhance computer performance.	B2, B4 (E)
<b>c1</b>	Design the main units of a digital computer according to the selected instruction set, and to meet desired performance.	C2 (E)
<b>c2</b>	Use simulation programs to design digital computer units.	C4 (E)
<b>d1</b>	Perform specific tasks individually and make a discussion group.	D1, D2, D4, D5 (E)
<b>d2</b>	Follow the standards to achieve his reports and presentations.	

<b>(A) Alignment Course Intended Learning Outcomes of Knowledge and Understanding to Teaching Strategies and Assessment Strategies</b>		
<b>Course Intended Learning Outcomes</b>	<b>Teaching strategies</b>	<b>Assessment Strategies</b>

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<b>a1</b> Classify and list the different types of computer organizations and architectures.	<ul style="list-style-type: none"> <li>▪ Lectures</li> <li>▪ Dialogue and discussion</li> <li>▪ Self-learning</li> </ul>	<ul style="list-style-type: none"> <li>▪ Written Test and Quizzes</li> <li>▪ Reports evaluation</li> <li>▪ Presentations evaluation</li> </ul>
<b>a2</b> Recognize the efficiency design of a digital computer and its units.	<ul style="list-style-type: none"> <li>▪ Lectures</li> <li>▪ Tutorials</li> </ul>	<ul style="list-style-type: none"> <li>▪ Written Test and Quizzes</li> <li>▪ Reports evaluation</li> </ul>

<b>(B) Alignment Course Intended Learning Outcomes of Intellectual Skills to Teaching Strategies and Assessment Strategies</b>		
<b>Course Intended Learning Outcomes</b>	<b>Teaching strategies</b>	<b>Assessment Strategies</b>
<b>b1</b> Compose the main units of a digital computer.	<ul style="list-style-type: none"> <li>▪ Lectures</li> <li>▪ Dialogue and discussion</li> <li>▪ Tutorials</li> </ul>	<ul style="list-style-type: none"> <li>▪ Written Test and Quizzes</li> <li>▪ Reports evaluation</li> </ul>
<b>b2</b> Evaluate the different solutions to enhance computer performance.	<ul style="list-style-type: none"> <li>▪ Lectures</li> <li>▪ Problems solving</li> </ul>	<ul style="list-style-type: none"> <li>▪ Written Test and Quizzes</li> <li>▪ Reports evaluation</li> </ul>

<b>(C) Alignment Course Intended Learning Outcomes of Professional and Practical Skills to Teaching Strategies and Assessment Strategies</b>		
<b>Course Intended Learning Outcomes</b>	<b>Teaching strategies</b>	<b>Assessment Strategies</b>
<b>c1</b> Design the main units of a digital computer according to the selected instruction set, and to meet desired performance.	<ul style="list-style-type: none"> <li>▪ Lectures</li> <li>▪ Dialogue and discussion.</li> <li>▪ Design exercises</li> </ul>	<ul style="list-style-type: none"> <li>▪ Written Test and Quizzes</li> <li>▪ Coursework assignments</li> </ul>

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<p><b>c2</b> Use simulation programs to design digital computer units.</p>	<ul style="list-style-type: none"> <li>▪ Lectures</li> <li>▪ Dialogue and discussion.</li> <li>▪ Design exercises</li> </ul>	<ul style="list-style-type: none"> <li>▪ Written Test and Quizzes</li> <li>▪ Coursework assignments</li> </ul>
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<b>(D) Alignment Course Intended Learning Outcomes of Transferable Skills to Teaching Strategies and Assessment Strategies</b>		
<b>Course Intended Learning Outcomes</b>	<b>Teaching strategies</b>	<b>Assessment Strategies</b>
<b>d1</b> Perform specific tasks individually and make a discussion group.	<ul style="list-style-type: none"> <li>▪ Tutorials</li> <li>▪ Self-learning</li> <li>▪ Dialogue and discussion</li> </ul>	<ul style="list-style-type: none"> <li>▪ Observation and interviews</li> <li>▪ Presentations evaluation</li> <li>▪ Reports evaluation</li> </ul>
<b>d2</b> Follow the standards to achieve his reports and presentations.	<ul style="list-style-type: none"> <li>▪ Presentation Self-learning</li> </ul>	<ul style="list-style-type: none"> <li>▪ Report, Presentations evaluation</li> <li>▪ Reports evaluation</li> </ul>

<b>V. Course Content</b>					
<b>A – Theoretical Aspect</b>					
<b>Order</b>	<b>Units/Topics List</b>	<b>Learning Outcomes</b>	<b>Sub Topics List</b>	<b>Number of Weeks</b>	<b>Contact hours</b>
<b>1.</b>	Introduction to Computer Architecture and Design	a1, a2, b1, b2, c1	<ul style="list-style-type: none"> <li>▪ Computer organization</li> <li>▪ Architecture</li> <li>▪ Function</li> <li>▪ Designing for performance</li> <li>▪ Computer component</li> <li>▪ Interconnection structure</li> </ul>	2	4
<b>2.</b>	Computer Memory System	a1, a2, b1, b2, c1, d1, d2	<ul style="list-style-type: none"> <li>▪ Memory system (Internal Memory, External Memory)</li> <li>▪ Cache memory principle</li> <li>▪ Elements of cache design</li> </ul>	3	6

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3.	I/O subsystems	a1, a2, b1, b2, c1, d1, d2	<ul style="list-style-type: none"> <li>▪ Input subsystem</li> <li>▪ Output subsystem</li> </ul>	2	4
4.	Instruction Sets	a1, a2, b1, b2, c1	<ul style="list-style-type: none"> <li>▪ Characteristics</li> <li>▪ Function</li> <li>▪ Addressing modes</li> <li>▪ Format</li> </ul>	2	4
5.	Processor Structure and Functions	a1, a2, b1, b2, c1	<ul style="list-style-type: none"> <li>▪ Processor organization</li> <li>▪ Register organization</li> </ul>	2	4
6.	Control Unit Operations	a2, b1, b2, c1	<ul style="list-style-type: none"> <li>▪ Fetch cycle</li> <li>▪ Indirect cycle</li> <li>▪ Execute cycle</li> <li>▪ Interrupt cycle</li> <li>▪ Instruction cycle</li> </ul>	2	4
7.	Introduction to Parallel Organization and Processing	a1, a2, b1, b2	<ul style="list-style-type: none"> <li>▪ Parallel organization</li> <li>▪ Parallel processing</li> </ul>	1	2
<b>Number of Weeks /and Units Per Semester</b>				<b>14</b>	<b>28</b>

<b>B - Tutorial Aspect</b>				
Order	Topics List	Number of Weeks	Contact hours	Learning Outcomes
1.	Introduction to Computer Architecture and Design (Computer organization, Architecture, Function, Designing for performance, Computer component, Interconnection structure)	2	4	a1, a2, b1, b2, c1
2.	Computer Memory System (Memory system (Internal Memory, External Memory), Cache memory principle, Elements of cache design)	3	6	a1, a2, b1, b2, c1, d1, d2

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3.	Input / Output subsystems	2	4	a1, a2, b1, b2, c1, d1, d2
4.	Instruction Sets (Characteristics, Function, Addressing modes, Format)	2	4	a1, a2, b1, b2, c1, c2
5.	Processor Structure and Functions (Processor organization, Register organization)	2	4	a1, a2, b1, b2, c1
6.	Control Unit Operations (Fetch cycle, Indirect cycle, Execute cycle, Interrupt cycle, Instruction cycle)	2	4	a2, b1, b2, c1, c2
7.	Introduction to Parallel Organization and Processing	1	2	a1, a2, b1, b2
<b>Number of Weeks /and Units Per Semester</b>		<b>14</b>	<b>28</b>	

<b>VI. Teaching strategies of the course</b>	
<ul style="list-style-type: none"> <li>• Lectures</li> <li>• Dialogue and discussion</li> <li>• Lectures</li> <li>• Tutorials</li> <li>• Problems solving</li> <li>• Design exercises</li> <li>• Self-learning</li> </ul>	

<b>VII. Assignments</b>				
No	Assignments	Aligned CILOs(symbols)	Week Due	Mark
1	Project Researches	a1, d1, d2	3 <sup>rd</sup>	5
2	Problems and Exercises Reports	a2, b1, b2, c1, c2, d1, d2	Weekly	5
<b>Total</b>				<b>10</b>

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<b>VIII. Schedule of Assessment Tasks for Students during the Semester</b>					
No.	Assessment Method	Week Due	Mark	Proportion of Final Assessment	Aligned Course Learning Outcomes
1.	Researches	3 <sup>rd</sup>	7.5	5%	a1, d1, d2
2.	Problems and Exercises Reports	Weekly	7.5	5%	a2, b1, b2, c1, c2, d1, d2
3.	Quizzes	4 <sup>th</sup> , 6 <sup>th</sup> , and 14 <sup>th</sup>	15	10%	a1, a2, b1, b2, c1
4.	Midterm Exam	8 <sup>th</sup>	30	20%	a1, a2, b1, b2, c1
5.	Final Exam	16 <sup>th</sup>	90	60%	a1, a2, b1, b2, c1
<b>Total</b>			<b>150</b>	<b>100%</b>	

<b>IX. Learning Resources</b>	
<i>Written in the following order: (Author - Year of publication – Title – Edition – Place of publication – Publisher).</i>	
<b>1- Required Textbook(s) (maximum two)</b>	
	1) W. Stalling, (2003), “Computer Organization & Architecture: Designing for Performance”, Prentice Hall. 2) M. Morris R. Mano (1992), “Computer System Architecture”, Third Edition, Pearson.
<b>2- Essential References</b>	
	1) John P. Hayes (1997), “Computer Architecture and Organization”, Third Edition. McGraw-Hill.
<b>3- Electronic Materials and Web Sites etc.</b>	

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X. Course Policies	
1.	<b>Class Attendance:</b> The regulations are applied, which state that a student who desires more than 25% of attending lectures is deprived of the final examination.
2.	<b>Tardy:</b> If the student is late for attending the lecture time, his degree will be deducted for each delay in the attendance grades.
3.	<b>Exam Attendance/Punctuality:</b> The student must rely on himself for exam.
4.	<b>Assignments &amp; Projects:</b> None.
5.	<b>Cheating:</b> If the student is caught cheating, he will be deprived of the exam in the subject.
6.	<b>Plagiarism:</b> In the case of student impersonation, the Vice Dean for Student Affairs will be referred to the College's Student Affairs Committee the necessary action.
7.	<b>Other Policies:</b> If the student does not attend more than 75% in the process, he will be deprived of the practical exam.

<b>Reviewed By</b>	<p><b><u>Vice Dean for Academic Affairs and Post Graduate Studies: Asst. Prof. Dr. Tarek A. Barakat</u></b></p> <p><b><u>President of Quality Assurance Unit: Assoc. Prof. Dr. Mohammed Algorafi</u></b></p> <p><b><u>Name of Reviewer from the Department: Asst. Prof. Dr. Adel Ahmed Al-Shakiri</u></b></p>
	<p><b><u>Deputy Rector for Academic Affairs Asst. Prof. Dr. Ibrahim AlMutaa</u></b></p> <p><b><u>Assoc. Prof. Dr. Ahmed Mujahed</u></b></p> <p><b><u>Asst. Prof. Dr. Munasar Alsubri</u></b></p>

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**Electrical Engineering Department**  
**B.Sc. of Computer and Control Engineering**



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